



PATENT
Atty. Docket No. ASC-044C1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Fitzgerald *et al.*

SERIAL NUMBER: 10/611,739

ART UNIT: 2818

FILING DATE: July 1, 2003

EXAMINER: Dung Anh Le

TITLE: METHOD OF FABRICATING CMOS INVERTER AND INTEGRATED
CIRCUITS UTILIZING STRAINED SILICON SURFACE CHANNEL
MOSFETS

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE

Sir:

In response to the Office action mailed on February 18, 2004, Applicants request the Examiner to consider the following:

Remarks begin on page 2 of this paper; and

Conclusion begins on page 2 of this paper.